

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	16	multiplierless and (IIR or recursive) and (ROM) and (shift near3 register) and adder	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 18:34
S2	121	IIR and (genetic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 17:37
S3	9	S2 and (power near4 ("2" or two))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 17:37
S4	3	multiplierless and filter and genetic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 17:40
S5	115	multiplierless and filter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:22
S6	53	S5 and ROM	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 17:40
S7	34	S6 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:27
S8	1	multiplierless and (first near2 shift near2 register) and (second near2 shift near2 register)and (barrel near3 shift\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:22

EAST Search History

S9	957	(adder near3 trees) and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:22
S10	671	S9 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:22
S11	4	S10 and multiplierless	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:28
S12	1	(US-4862402-\$).did.	USPAT	OR	ON	2006/12/09 19:26
S13	2392	("32" near3 shifters)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:30
S14	1973	S13 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:31
S15	0	S14 and multiplierless	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:28
S16	28	S14 and (IIR or recursive)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:32
S17	4	("32 barrel shifters")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:31

EAST Search History

S18	205	(plurality with (barrel adj2 shifters))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:31
S19	159	S18 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:39
S20	5	S19 and (IIR or recursive)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:32
S21	1	(US-20020126858-\$).did.	US-PGPUB	OR	ON	2006/12/09 19:38
S22	56	IIR and (multiplexer with (shift near2 register))	US-PGPUB	OR	ON	2006/12/09 19:45
S23	5	S22 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:45
S24	76	IIR and (multiplexer same (shift near2 register))	US-PGPUB	OR	ON	2006/12/09 19:45
S25	9	S24 and @pd<"20030708"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/09 19:45
S26	2	(US-20020131487-\$ or US-20030083852-\$).did.	US-PGPUB	OR	ON	2006/12/09 19:56


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

☐ Search Results

[BROWSE](#)
[SEARCH](#)
[IEEE XPLORE GUIDE](#)

Results for "((yu<in>au) <and> (multiplierless<in>ti))"

☐ e-mail

Your search matched 4 of 1436708 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set

 Display Format: ☒ Citation ☐ Citation & Abstract

» Key



Indicates full text access

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **Efficient VLSI Implementations of Fast Multiplierless Approximated DCT I Parameterized Hardware Modules for Silicon Intellectual Property Design**
 Shen-Fu Hsiao; Yu Hen Hu; Juang, T.-B.; Chung-Han Lee;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circu Fundamental Theory and Applications, IEEE Transactions on]
 Volume 52, Issue 8, Aug. 2005 Page(s):1568 - 1579
 Digital Object Identifier 10.1109/TCSI.2005.851709
 Abstract | Full Text: [PDF\(896 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 2. **Genetic algorithm approach for the optimization of multiplierless sub-filters the frequency-response masking technique**
 Ya Jun Yu; Yong Ching Lim;
Electronics, Circuits and Systems, 2002. 9th International Conference on
 Volume 3, 15-18 Sept. 2002 Page(s):1163 - 1166 vol.3
 Digital Object Identifier 10.1109/ICECS.2002.1046459
 Abstract | Full Text: [PDF\(340 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **New natural selection process and chromosome encoding for the design lattice QMF using genetic algorithm**
 Ya Jun Yu; Yong Ching Lim;
Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International
 on
 Volume 3, 2-5 Sept. 2001 Page(s):1273 - 1276 vol.3
 Digital Object Identifier 10.1109/ICECS.2001.957447
 Abstract | Full Text: [PDF\(328 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Low-power multiplierless FIR filter synthesizer based on CSD code**
 Maw-Ching Liu; Chien-Lung Chen; Ding-Yu Shin; Chin-Hung Lin; Shyh-Jye Joo
Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Sympos
 Volume 4, 6-9 May 2001 Page(s):666 - 669 vol. 4
 Digital Object Identifier 10.1109/ISCAS.2001.922325
 Abstract | Full Text: [PDF\(352 KB\)](#) IEEE CNF
[Rights and Permissions](#)



[Help](#) [Contact Us](#) [Privacy & :](#)

© Copyright 2006 IEEE –


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((bhattacharya<in>au) <and> (multiplierless<in>ti))"

☒ e-mail

Your search matched 13 of 1436708 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **Fourth-order structures for multiplierless realizations of bandpass and bandstop filters transformed from all-pole lowpass filters**
 Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
 23-26 May 2005 Page(s):2599 - 2602 Vol. 3
 Digital Object Identifier 10.1109/ISCAS.2005.1465158
[AbstractPlus](#) | [Full Text: PDF\(184 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Multiplierless realization of bandpass and bandstop digital filters transformed from all-pole lowpass filters**
 Bhattacharya, M.; Saramaki, T.;
Acoustics, Speech, and Signal Processing, 2005. Proceedings. (ICASSP '05). International Conference on
 Volume 5, 18-23 March 2005 Page(s):v/113 - v/116 Vol. 5
 Digital Object Identifier 10.1109/ICASSP.2005.1416253
[AbstractPlus](#) | [Full Text: PDF\(209 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Multiplierless implementation of bandpass and bandstop recursive digital allpass structures**
 Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
 Volume 4, 25-28 May 2003 Page(s):IV-249 - IV-252 vol.4
 Digital Object Identifier 10.1109/ISCAS.2003.1205820
[AbstractPlus](#) | [Full Text: PDF\(429 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Allpass structures for multiplierless realization of recursive digital filters**
 Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
 Volume 4, 25-28 May 2003 Page(s):IV-237 - IV-240 vol.4
 Digital Object Identifier 10.1109/ISCAS.2003.1205817
[AbstractPlus](#) | [Full Text: PDF\(405 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Some observations on multiplierless implementation of linear phase FIR**
 Bhattacharya, M.; Saramaki, T.;

Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Volume 4, 25-28 May 2003 Page(s):IV-193 - IV-196 vol.4
Digital Object Identifier 10.1109/ISCAS.2003.1205806

AbstractPlus | Full Text: PDF(402 KB) IEEE CNF
Rights and Permissions

- ☐ **6. Some observations leading to multiplierless implementation of linear phase filters**
Bhattacharya, M.; Saramaki, T.;
Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03). International Conference on
Volume 2, 6-10 April 2003 Page(s):II - 517-20 vol.2
Digital Object Identifier 10.1109/ICASSP.2003.1202417
AbstractPlus | Full Text: PDF(324 KB) IEEE CNF
Rights and Permissions
- ☐ **7. Multiplierless realization of recursive digital filters using allpass structures**
Saramaki, T.; Bhattacharya, M.;
Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03). International Conference on
Volume 2, 6-10 April 2003 Page(s):II - 505-8 vol.2
Digital Object Identifier 10.1109/ICASSP.2003.1202414
AbstractPlus | Full Text: PDF(313 KB) IEEE CNF
Rights and Permissions
- ☐ **8. Multiplierless realization of all pole digital filters**
Saramaki, T.; Bhattacharya, M.;
Signal Processing, 2002 6th International Conference on
Volume 1, 26-30 Aug. 2002 Page(s):1 - 4 vol.1
Digital Object Identifier 10.1109/ICOSP.2002.1180968
AbstractPlus | Full Text: PDF(326 KB) IEEE CNF
Rights and Permissions
- ☐ **9. Multiplierless implementation of all-pole digital filters**
Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 2, 26-29 May 2002 Page(s):II-696 - II-699 vol.2
Digital Object Identifier 10.1109/ISCAS.2002.1011448
AbstractPlus | Full Text: PDF(425 KB) IEEE CNF
Rights and Permissions
- ☐ **10. Multiplierless implementation of bandpass and bandstop recursive digital filters**
Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 2, 26-29 May 2002 Page(s):II-692 - II-695 vol.2
Digital Object Identifier 10.1109/ISCAS.2002.1011447
AbstractPlus | Full Text: PDF(433 KB) IEEE CNF
Rights and Permissions
- ☐ **11. Multiplierless implementation of bandpass and bandstop IIR digital filters**
Bhattacharya, M.; Saramaki, T.;
Acoustics, Speech, and Signal Processing, 2002. Proceedings. (ICASSP '02). International Conference on
Volume 3, 13-17 May 2002 Page(s):III-3184 - III-3187 vol.3
Digital Object Identifier 10.1109/ICASSP.2002.1005364
AbstractPlus | Full Text: PDF(416 KB) IEEE CNF
Rights and Permissions
- ☐ **12. Multiplierless implementation of recursive digital filters using a class of IIR structures**

Bhattacharya, M.; Astola, J.; Saramaki, T.;
Signal Processing and its Applications, Sixth International, Symposium on, 2001
Volume 2, 13-16 Aug. 2001 Page(s):611 - 614 vol.2
Digital Object Identifier 10.1109/ISSPA.2001.950219
[AbstractPlus](#) | Full Text: [PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)



13. Multiplierless implementation of recursive digital filters based on coefficient methods in low sensitivity structures
Bhattacharya, M.; Astola, J.;
Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on
Volume 2, 6-9 May 2001 Page(s):697 - 700 vol. 2
Digital Object Identifier 10.1109/ISCAS.2001.921166
[AbstractPlus](#) | Full Text: [PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)

Indexed by
 Inspec

[Help](#) [Contact Us](#) [Privacy & Policy](#)

© Copyright 2006 IEEE – All Rights Reserved


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

☐ Search Results
[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)

Results for "((multiplierless and (recursive or iir) and (genetic))<in>metadata)"

☒ e-mail

Your search matched 1 of 1436708 documents.

A maximum of 100 results are displayed, 100 to a page, sorted by Publication year in Descending order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

[Select All](#) [Deselect All](#)

- ☐ 1. Design of 2-D multiplierless IIR filters using the genetic algorithm
 Thamvichai, R.; Bose, T.; Haupt, R.L.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
Circuits and Systems I: Regular Papers, IEEE Transactions on]
 Volume 49, Issue 6, June 2002 Page(s):878 - 882
 Digital Object Identifier 10.1109/TCSI.2002.1010045
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(296 KB\)](#) IEEE JNL
[Rights and Permissions](#)

 Indexed by
[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2006 IEEE -


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((multiplierless and (recursive or iir))<in>metadata)"

☐ e-mail

Your search matched 34 of 1436708 documents.

A maximum of 100 results are displayed, 100 to a page, sorted by Publication year in Descending order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **Cascaded multiplierless realizations of low sensitivities allpass based fra filters**
 Ivanova, K.S.; Anzova, V.I.; Stoyanov, G.K.;
Telecommunications in Modern Satellite, Cable and Broadcasting Services, 20 International Conference on
 Volume 1, 28-30 Sept. 2005 Page(s):111 - 114 vol. 1
 Digital Object Identifier 10.1109/TELSKS.2005.1572074
[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **A systematic algorithm for designing multiplierless computationally effic decimators and interpolators**
 Yli-Kaakinen, J.; Saramaki, T.;
Image and Signal Processing and Analysis, 2005. ISPA 2005. Proceedings of International Symposium on
 15-17 Sept. 2005 Page(s):167 - 172
[AbstractPlus](#) | Full Text: [PDF\(1277 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Fourth-order structures for multiplierless realizations of bandpass and b filters transformed from all-pole lowpass filters**
 Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
 23-26 May 2005 Page(s):2599 - 2602 Vol. 3
 Digital Object Identifier 10.1109/ISCAS.2005.1465158
[AbstractPlus](#) | Full Text: [PDF\(184 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 4. **Factorization of a class of perfect reconstruction modified DFT filter bank**
 Yin, S.S.; Chan, S.C.;
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on
 23-26 May 2005 Page(s):4313 - 4316 Vol. 5
 Digital Object Identifier 10.1109/ISCAS.2005.1465585
[AbstractPlus](#) | Full Text: [PDF\(216 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **Multiplierless realization of bandpass and bandstop digital filters transfo pole lowpass filters**

Bhattacharya, M.; Saramaki, T.;
Acoustics, Speech, and Signal Processing, 2005. Proceedings. (ICASSP '05).
International Conference on
Volume 5, 18-23 March 2005 Page(s):v/113 - v/116 Vol. 5
Digital Object Identifier 10.1109/ICASSP.2005.1416253
[AbstractPlus](#) | [Full Text: PDF\(209 KB\)](#) IEEE CNF
[Rights and Permissions](#)

6. **Design of multiplierless elliptic narrowband IIR digital filter based on sen**
Trirat, A.; Chivapreecha, S.; Khunaworawet, T.; Ruangrangsarn, T.; Dejhan, K.;
Communications and Information Technology, 2004. ISCIT 2004. IEEE Interna
on
Volume 1, 26-29 Oct. 2004 Page(s):171 - 177 vol.1
Digital Object Identifier 10.1109/ISCIT.2004.1412473
[AbstractPlus](#) | [Full Text: PDF\(674 KB\)](#) IEEE CNF
[Rights and Permissions](#)
7. **Multiplierless implementation of bandpass and bandstop recursive digita
allpass structures**
Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
Volume 4, 25-28 May 2003 Page(s):IV-249 - IV-252 vol.4
Digital Object Identifier 10.1109/ISCAS.2003.1205820
[AbstractPlus](#) | [Full Text: PDF\(429 KB\)](#) IEEE CNF
[Rights and Permissions](#)
8. **Multiplierless realization of recursive digital filters using allpass structure**
Saramaki, T.; Bhattacharya, M.;
Acoustics, Speech, and Signal Processing, 2003. Proceedings. (ICASSP '03).
International Conference on
Volume 2, 6-10 April 2003 Page(s):II - 505-8 vol.2
Digital Object Identifier 10.1109/ICASSP.2003.1202414
[AbstractPlus](#) | [Full Text: PDF\(313 KB\)](#) IEEE CNF
[Rights and Permissions](#)
9. **Allpass structures for multiplierless realization of recursive digital filters**
Bhattacharya, M.; Saramaki, T.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
Volume 4, 25-28 May 2003 Page(s):IV-237 - IV-240 vol.4
Digital Object Identifier 10.1109/ISCAS.2003.1205817
[AbstractPlus](#) | [Full Text: PDF\(405 KB\)](#) IEEE CNF
[Rights and Permissions](#)
10. **Multiplierless predictor for DPCM of images**
Thamvichai, R.; Bose, T.; Radenkovic, M.;
Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International
Volume 2, 25-28 May 2003 Page(s):II-388 - II-391 vol.2
[AbstractPlus](#) | [Full Text: PDF\(379 KB\)](#) IEEE CNF
[Rights and Permissions](#)
11. **Design of 2-D multiplierless IIR filters using the genetic algorithm**
Thamvichai, R.; Bose, T.; Haupt, R.L.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transacti
Circuits and Systems I: Regular Papers, IEEE Transactions on]
Volume 49, Issue 6, June 2002 Page(s):878 - 882
Digital Object Identifier 10.1109/TCSI.2002.1010045
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(296 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☒ **12. Design of high-speed multiplierless filters using a nonrecursive signed coefficient subexpression algorithm**
Martinez-Peiro, M.; Boemo, E.I.; Wanhammar, L.;
[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on](#)
[Circuits and Systems II: Express Briefs, IEEE Transactions on](#)
Volume 49, Issue 3, March 2002 Page(s):196 - 203
Digital Object Identifier 10.1109/TCSII.2002.1013866
[AbstractPlus](#) | [Full Text: PDF\(319 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **13. Multiplierless implementation of bandpass and bandstop recursive digital filters**
Bhattacharya, M.; Saramaki, T.;
[Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on](#)
Volume 2, 26-29 May 2002 Page(s):II-692 - II-695 vol.2
Digital Object Identifier 10.1109/ISCAS.2002.1011447
[AbstractPlus](#) | [Full Text: PDF\(433 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **14. Multiplierless implementation of bandpass and bandstop IIR digital filters**
Bhattacharya, M.; Saramaki, T.;
[Acoustics, Speech, and Signal Processing, 2002. Proceedings. \(ICASSP '02\). International Conference on](#)
Volume 3, 13-17 May 2002 Page(s):III-3184 - III-3187 vol.3
Digital Object Identifier 10.1109/ICASSP.2002.1005364
[AbstractPlus](#) | [Full Text: PDF\(416 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **15. Using 2nd-order information to reduce average coefficient length in multiplierless filters**
DeBrunner, V.; DeBrunner, L.S.; Mohan, A.;
[Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on](#)
Volume 2, 26-29 May 2002 Page(s):II-189 - II-192 vol.2
Digital Object Identifier 10.1109/ISCAS.2002.1010956
[AbstractPlus](#) | [Full Text: PDF\(525 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **16. Multiplierless implementation of recursive digital filters using a class of IIR structures**
Bhattacharya, M.; Astola, J.; Saramaki, T.;
[Signal Processing and its Applications, Sixth International, Symposium on. 2001](#)
Volume 2, 13-16 Aug. 2001 Page(s):611 - 614 vol.2
Digital Object Identifier 10.1109/ISSPA.2001.950219
[AbstractPlus](#) | [Full Text: PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **17. Design and multiplierless implementation of two-channel biorthogonal IIR filters with low system delay**
Mao, J.S.; Lu, W.-S.; Chan, S.C.; Antoniou, A.;
[Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on](#)
Volume 2, 6-9 May 2001 Page(s):465 - 468 vol. 2
Digital Object Identifier 10.1109/ISCAS.2001.921108
[AbstractPlus](#) | [Full Text: PDF\(288 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **18. Multiplierless implementation of recursive digital filters based on coefficient quantization methods in low sensitivity structures**
Bhattacharya, M.; Astola, J.;
[Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on](#)
Volume 2, 6-9 May 2001 Page(s):697 - 700 vol. 2
Digital Object Identifier 10.1109/ISCAS.2001.921166

[AbstractPlus](#) | [Full Text: PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ 19. **Multiplierless realization of recursive digital filters**
Bhattacharaya, M.; Saramaki, T.; Astola, J.;
[Image and Signal Processing and Analysis, 2001. ISPA 2001. Proceedings of International Symposium on](#)
19-21 June 2001 Page(s):469 - 474
Digital Object Identifier 10.1109/ISPA.2001.938675
[AbstractPlus](#) | [Full Text: PDF\(384 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ 20. **Novel recursive-DCT implementations: a comparative study**
Demirsoy, S.S.; Beck, R.; Kale, I.; Dempster, A.G.;
[Intelligent Data Acquisition and Advanced Computing Systems: Technology at International Workshop on, 2001.](#)
1-4 July 2001 Page(s):120 - 123
Digital Object Identifier 10.1109/IDAACS.2001.941994
[AbstractPlus](#) | [Full Text: PDF\(288 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ 21. **Design of discrete coefficient FIR and IIR digital filters with prefilter-equalization using linear programming**
Oh, H.J.; Lee, Y.H.;
[Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on](#)
[Circuits and Systems II: Express Briefs, IEEE Transactions on](#)
Volume 47, Issue 6, June 2000 Page(s):562 - 565
Digital Object Identifier 10.1109/82.847076
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(148 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ 22. **Approximate linear phase multiplierless IIR halfband filter**
Lutovac, M.D.; Milic, L.D.;
[Signal Processing Letters, IEEE](#)
Volume 7, Issue 3, March 2000 Page(s):52 - 53
Digital Object Identifier 10.1109/97.823523
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(48 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ 23. **An algorithm for the design of multiplierless approximately linear-phase digital filters**
Yli-Kaakinen, J.; Saramaki, T.;
[Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Symposium on](#)
Volume 2, 28-31 May 2000 Page(s):77 - 80 vol.2
Digital Object Identifier 10.1109/ISCAS.2000.856262
[AbstractPlus](#) | [Full Text: PDF\(328 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ 24. **Design of multiplierless elliptic IIR filters with a small quantization error**
Milic, L.D.; Lutovac, M.D.;
[Signal Processing, IEEE Transactions on \[see also Acoustics, Speech, and Signal Processing, IEEE Transactions on\]](#)
Volume 47, Issue 2, Feb. 1999 Page(s):469 - 479
Digital Object Identifier 10.1109/78.740130
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(432 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ 25. **Local search algorithm for the design of multiplierless digital filters with**

coefficients

Xu, X.; Nowrouzian, B.;

Electrical and Computer Engineering, 1999 IEEE Canadian Conference on

Volume 2, 9-12 May 1999 Page(s):811 - 816 vol.2

Digital Object Identifier 10.1109/CCECE.1999.808066

AbstractPlus | Full Text: [PDF\(400 KB\)](#) IEEE CNF[Rights and Permissions](#)

- ☐ **26. Design of multiplierless elliptic IIR filters**
Milic, L.D.; Lutovac, M.D.;
Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on
Volume 3, 21-24 April 1997 Page(s):2201 - 2204 vol.3
Digital Object Identifier 10.1109/ICASSP.1997.599487

AbstractPlus | Full Text: [PDF\(312 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **27. Multiplierless structure for maximally flat linear phase FIR filters**
Samadi, S.; Cooklev, T.; Nishihara, A.; Fujii, N.;
Electronics Letters
Volume 29, Issue 2, 21 Jan. 1993 Page(s):184 - 185

AbstractPlus | Full Text: [PDF\(132 KB\)](#) IEE JNL
- ☐ **28. A mixed analog-digital realization of the LMS algorithm using sigma-delta**
Mueller, D.; Kaelin, A.; Moschytz, G.S.;
Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium
3-6 May 1993 Page(s):882 - 885 vol.1
Digital Object Identifier 10.1109/ISCAS.1993.393865

AbstractPlus | Full Text: [PDF\(316 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **29. IIR filters with reduced multipliers using cyclotomic polynomial numerators**
Harnett, R.J.; Jackson, L.B.; Boudreaux-Bartels, G.F.;
Acoustics, Speech, and Signal Processing, 1992. ICASSP-92., 1992 IEEE International Conference on
Volume 4, 23-26 March 1992 Page(s):321 - 324 vol.4
Digital Object Identifier 10.1109/ICASSP.1992.226371

AbstractPlus | Full Text: [PDF\(296 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **30. Implementation of recursive filters using highly quantized periodically time-varying coefficients**
Ghanekar, S.; Tantaratana, S.; Franks, L.E.;
Acoustics, Speech, and Signal Processing, 1991. ICASSP-91., 1991 International Conference on
14-17 April 1991 Page(s):1625 - 1628 vol.3
Digital Object Identifier 10.1109/ICASSP.1991.150581

AbstractPlus | Full Text: [PDF\(500 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **31. A low-complexity multiplierless half-band recursive digital filter design**
Samueli, H.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing]
Volume 37, Issue 3, March 1989 Page(s):442 - 444
Digital Object Identifier 10.1109/29.21716

AbstractPlus | Full Text: [PDF\(272 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- **32. ASIC design of digital ECG filter**
Williams, M.; Nurmi, J.; Tenhunen, H.;
ASIC Seminar and Exhibit, 1989. Proceedings., Second Annual IEEE
25-28 Sept. 1989 Page(s):P7 - 3/1-4
Digital Object Identifier 10.1109/ASIC.1989.123211
AbstractPlus | Full Text: PDF(296 KB) IEEE CNF
Rights and Permissions
- **33. Verification of quantization error formulas for ROM/ACC filters: Common realizations for narrow-band recursive digital filters"**
Munson, D., Jr.; Yong Hwang;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Sig
IEEE Transactions on
Volume 30, Issue 4, Aug 1982 Page(s):660 - 661
AbstractPlus | Full Text: PDF(232 KB) IEEE JNL
Rights and Permissions
- **34. ROM/ACC realization of digital filters for poles near the unit circle**
Munson, D.; Liu, B.;
Circuits and Systems, IEEE Transactions on
Volume 27, Issue 2, Feb 1980 Page(s):147 - 151
AbstractPlus | Full Text: PDF(504 KB) IEEE JNL
Rights and Permissions



[Help](#) [Contact Us](#) [Privacy & ;](#)

© Copyright 2006 IEEE –